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Toward Teraflop Computing and New Grand Challenge Applications

SOLVING LINEAR RECURRENCE SYSTEMS ON PARALLEL COMPUTERS

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Abstract. Two divide and conquer algorithms for the solution of a linear recurrence system of order m of n equations are introduced. The arithmetical complexity of the proposed algorithms is discussed. It is shown that for large n the optimal number of processors is $O(\sqrt{n})$ and that for the optimal number of processors the time complexity of the proposed algorithms is $O(\sqrt{n})$. It is also shown that for fixed n and fixed number of processors p the theoretical speed-up decreases as the order of the system m increases. The results of experiments on a 20-processor Sequent are presented.

Keywords. Linear recurrence systems, parallel algorithms, divide-and-conquer, shared-memory multiprocessors, speedup.

1. INTRODUCTION

The final phase of several numerical algorithms reduces to the solution of a linear recurrence system of order m for n equations. Many parallel algorithms for solving this problem have been proposed [1-4, 6-9, 11, 12]. In [12] two medium grain efficient divide and conquer parallel algorithms were presented and their theoretical implementation on a linear array of processors with message passing facilities was discussed. It has been recently shown that both algorithms have very good numerical properties [13].

The major purpose of this paper is to show that these algorithms can be efficiently implemented on shared memory MIMD multiprocessors. Section 2 contains a short description of the proposed algorithms. Their arithmetical complexity functions are presented and discussed in Section 3. In Section 4 the results of the experiments on a 20-processor Sequent Symmetry are presented and analyzed.

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2. PARALLEL ALGORITHMS

Let us define a linear recurrence system of order m for n equations, where $m \leq n$:

$$x_{k} = \begin{cases} 0 & \text{if } k \leq 0, \\ f_{k} + \sum_{j=k-m}^{k-1} a_{kj}x_{j} & \text{if } 1 \leq k \leq n. \end{cases}$$
 (1)

Computation of values x_k reduces to the system of linear equations

$$(I - A)x = f$$
 where $I, A \in \mathbb{R}^{n \times n}$ and $x, f \in \mathbb{R}^n$, (2)

where $\mathbf{x} = (\mathbf{z}_1, \dots, \mathbf{z}_n)^T$, $\mathbf{f} = (f_1, \dots, f_n)^T$ and $A = (a_{ik})$ with $a_{ik} = 0$ for $i \leq k$ or i - k > m.

Without loss of generality we can assume that n is divisible by p (where 'p' is the number of processors) and q = n/p > m. The system (2) can be written in the following block form

where $L_j \in R^{q \times q}$, x_j , $f_j \in R^q$ for j = 1, ..., p and $U_j \in R^{q \times q}$ for j = 2, ..., p. Vectors x_j satisfy the following recurrence relation

$$\begin{cases} \mathbf{x}_1 = L_1^{-1} \mathbf{f}_1, \\ \mathbf{x}_j = L_j^{-1} \mathbf{f}_j - L_j^{-1} U_j \mathbf{x}_{j-1} & \text{for } j = 2, ..., p. \end{cases}$$
(4)

Let U_j^k denote the kth column of the matrix U_j . Since $U_j^k = 0$ for j = 2, ..., p and k = 1, ..., q - m we obtain the following formula for Algorithm 1:

$$\begin{cases} \mathbf{x}_{1} = L_{1}^{-1} \mathbf{f}_{1}, \\ \mathbf{x}_{j} = \mathbf{x}_{j} - \sum_{k=0}^{m-1} \mathbf{z}_{(j-1)q-k} \mathbf{y}_{j}^{k} & \text{for } j = 2, ..., p, \end{cases}$$
 (5)

where $L_j \mathbf{x}_j = \mathbf{f}_j$ and $L_j \mathbf{y}_j^k = U_j^{q-k}$ for $k = 0, \ldots, m-1$.

To develop the second algorithm let us observe that each matrix U_j can be written as

$$U_{j} = -\sum_{k=1}^{m} \sum_{l=k}^{m} \beta_{j}^{kl} e_{k} e_{q-m+l}^{T}, \qquad (6)$$

where $\beta_j^{k!} = a_{(j-1)q+k,(j-1)q-m+l}$ and e_k denotes the kth unit vector of R^q . Substituting into (5) we obtain the following formula for Algorithm 2 (for more detailed description of both algorithms see [12])

$$\begin{cases} \mathbf{x}_{1} = L_{1}^{-1} \mathbf{f}_{1}, \\ \mathbf{x}_{j} = \mathbf{s}_{j} + \sum_{k=1}^{m} \alpha_{j}^{k} \mathbf{y}_{j}^{k} & \text{for } j = 2, \dots, p, \end{cases}$$
 (7)

where $\alpha_j^k = \sum_{l=k}^m \beta_j^{kl} \mathbf{e}_{q-m+l}^T \mathbf{x}_{j-1}$, $L_j \mathbf{y}_j^k = \mathbf{e}_k$ for $k = 1, \dots, m$, and $L_j \mathbf{s}_j = \mathbf{f}_j$.

Methods (5), (7) are examples of divide-and-conquer algorithms. First, several linear recurrence systems of order m for q equations are to be solved separately (in parallel). Second, a set of values needs to be calculated in each block and communicated to the next block effectively decoupling the original system. This is the sequential part of all three algorithms. Finally, the solution to the original problem is calculated independently in each block (in parallel).

3. COMPLEXITY OF THE ALGORITHMS

Let us first modify for the shared memory computer the arithmetical complexity formulas for (1) formally developed in [12]. Let τ_a denote the time of the execution of the basic arithmetic operation multiply and add. The complexity of a sequential algorithm for solving (1) can be expressed by the following formula

$$T_1(n) = m\left(n - \frac{m+1}{2}\right)\tau_a. \tag{8}$$

The complexity of Algorithm 1 is represented by:

$$T_{p,1}(n) = m \left[mp + (m+2) \frac{n}{p} - \frac{1}{2} (m^2 + 6m + 1) \right] \tau_{\bullet}^{-}, \tag{9}$$

whereas the complexity of Algorithm 2 is defined by:

$$T_{p,2}(n) = m \left[\left(\frac{3}{2}m + \frac{1}{2} \right) p + (m+2) \frac{n}{p} - \left(m^2 + \frac{9}{2}m + \frac{1}{2} \right) \right] \tau_a, \tag{10}$$

A number of observations can be made about the above complexity functions.

- The arithmetical complexity functions for the parallel algorithms have the same overall structure as the functions for other divide and conquer algorithms (see for instance [5, 10]).
- 2. Algorithm 2 gives better time complexity results for a small number of processors.
- 3. For small m and large n the optimal number of processors for both parallel algorithms is of order $O(\sqrt{n})$.
- 4. For small m, large n and an optimal number of processors $p_{opt} = O(\sqrt{n})$ both parallel algorithms perform in time $O(\sqrt{n})$.
- 5. For fixed p and m the increase in n does not increase the speedup.
- 6. For fixed n and p the increase of m decreases the speedup.

The last two properties are illustrated on Figure 1 which represents the predicted speedup for various values of m, optimal and fixed p and for $n = 1000, \ldots, 10,000$. It should be noted that for n = 10,000 the optimal number of processors $p_{opt} \approx 100$.

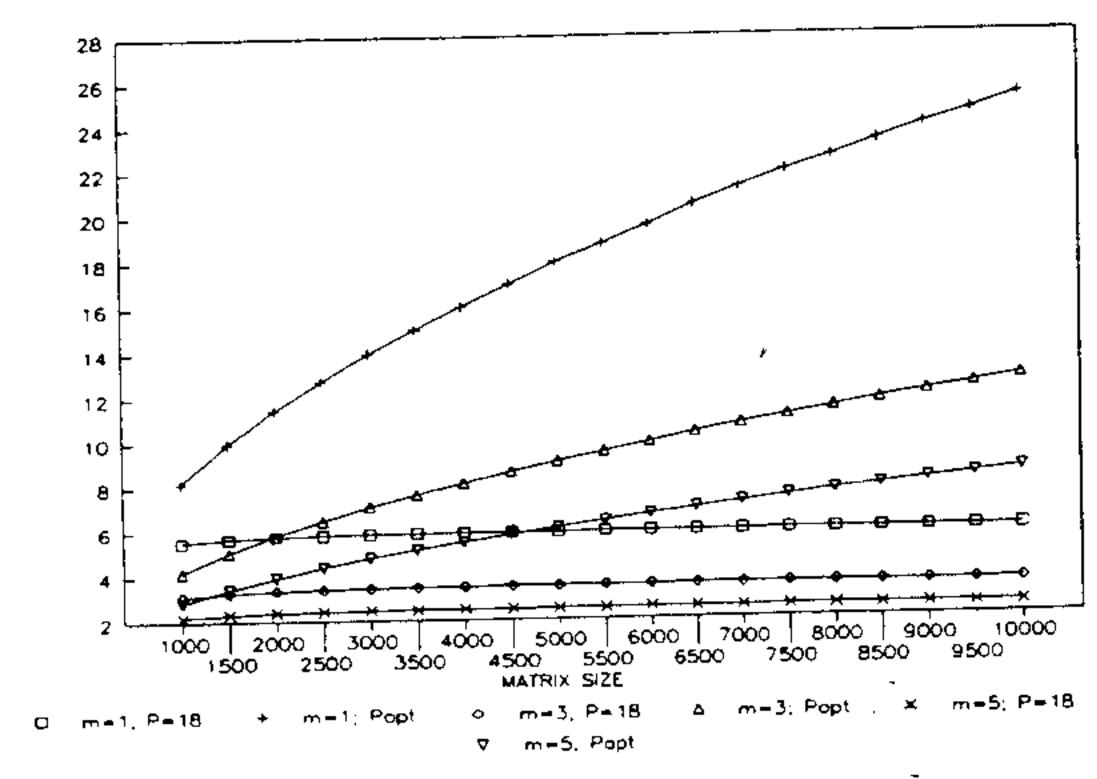


Figure 1: Effects of changes in p, m and n on the theoretical speedup.

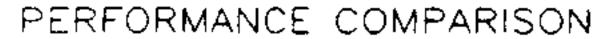
4. RESULTS

The experiments were performed on a 20 processor Sequent Symmetry. The code was implemented using Fortran 77 and Sequent provided parallelisation primitives (DOACROSS). Each result presented here is an average of multiple runs on an empty machine. For programming simplicity it was assumed that n is divisible by p. Since one of the processors is running the operating system and since 19 is not an optimal divisor, the largest number of processors used was 18.

Figure 2 presents the comparison between speedups of the two proposed algorithms for n=6930 (which was the biggest size we were able to fit into the memory for m=8), m=1, 4 and 8, and $p=2,\ldots,18$. Speedup of both algorithms was calculated against the same sequential algorithm.

It should be pointed out that the wavy shape of the curves is partially caused by the fact that the experiments were not carried out when the number of processors p was not a divisor of the system size n. As predicted, for increasing m the practical speedup decreases. There is no performance improvement for p=2 as the overhead of the parallel solution is bigger than the gains from parallelisation. As m increases it takes more and more processors to obtain any speedup. It can be also observed that, in all cases, Algorithm 2 gives better performance than Algorithm 1. This is a general result that we have observed in all experiments with large n and for which we do not have a complete explanation. From now on we will report the performance of Algorithm 2 only.

Figure 3 compares the performance of Algorithm 2 for m=1 for various matrix sizes. A couple of observations can be made. First, the system overhead can be observed for p>12 for matrix sizes up to n=5040. Only for the largest system size



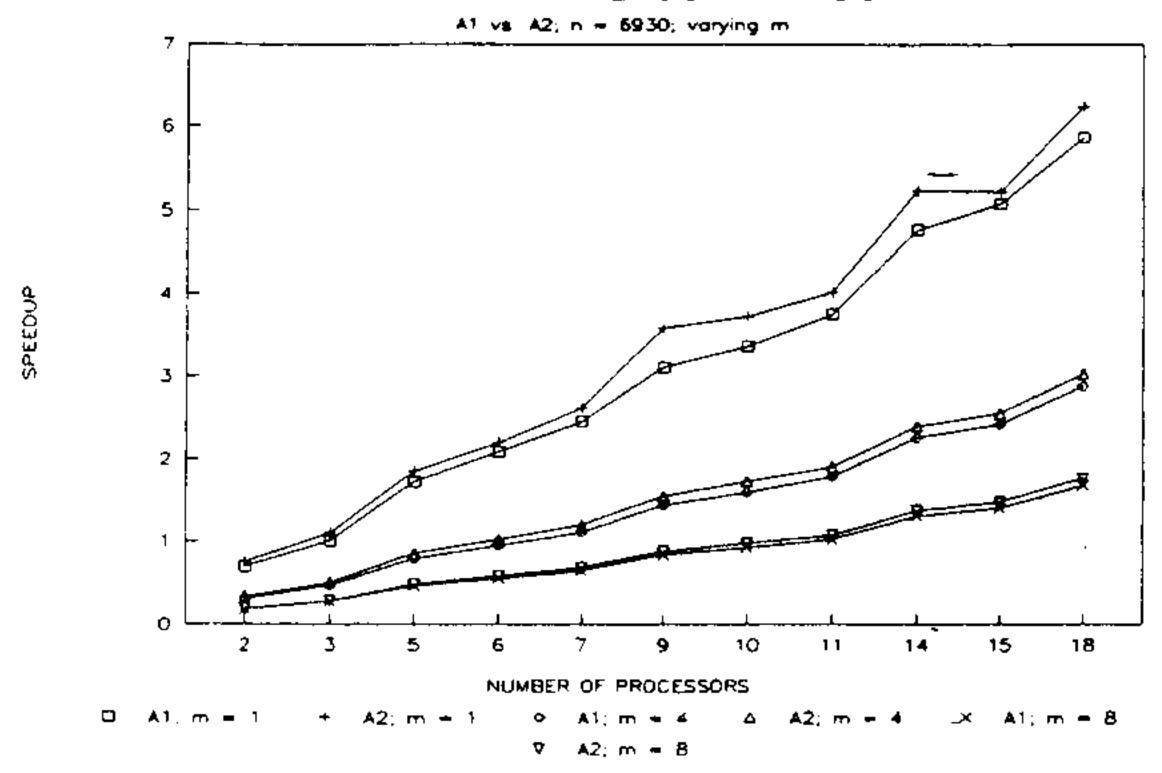
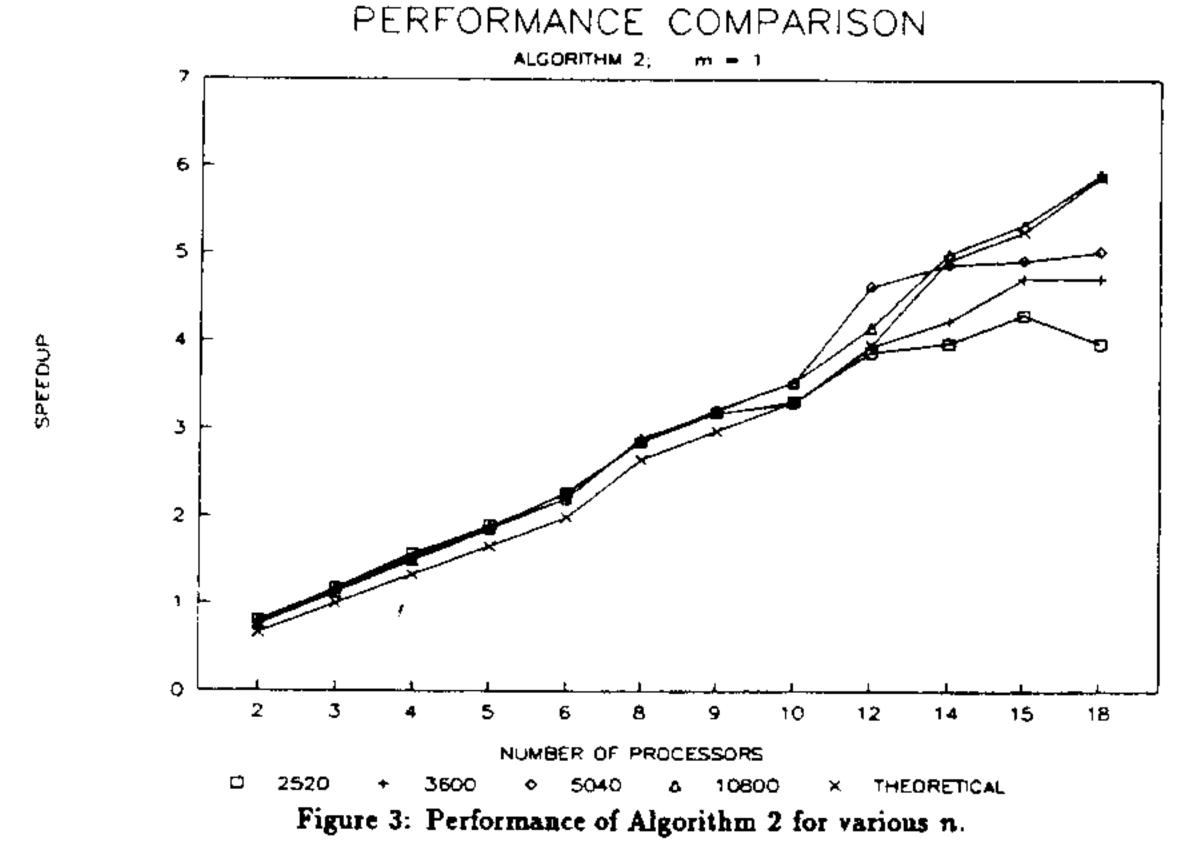


Figure 2: Comparison between the two algorithms for various m.



the levelling-off effect disappears. Second, as it was predicted theoretically, for a given number of processors the increase in n does not produce speedup increase. Third, the theoretical functions developed in Section 3 provide surprisingly good speedup predictions (the difference between the actual and predicted speedup is smaller than 5%).

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